Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	719/300.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:32
L2	890	719/310.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:32
L3	63	l2 and latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:32
L4	1374	709/200.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:32
L5	178	l4 and latency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:32
L6	74	l5 and transaction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:33
L7	26337	709/201-203,217-227.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:33
L8	660	705/65,75.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:33

L9	1796	718/100,101.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:33
L10	201	902/22.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:33
L11	3391	719/310,313-318.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L12	560	700/32,91.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L13	1308	702/182.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L14	695	703/22.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L15	933	705/11,22.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L16	1018	714/47.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34

L17	4545	709/224.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L18	1367	710/15-18.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:34
L19	247	715/736.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:35
L20	767	717/127,128.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:35
L21	37455	l7 or l8 or l9 or l10 or l11 or l12 or l13 or l14 or l15 or l16 or l17 or l18 or l19 or l20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:35
L22	337	l21 and latency and transaction same event	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:36
L23	173	l22 and agent	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:36
L24	52	l23 and raw near5 data	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/03 16:36
S1	761	709/200.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:07

S2	3	finnerty-warren$.in.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:10
S3	31	goldman adj sach$.as.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:10
S4	0	(goldman adj sach$.as.) and API and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:11
S5	1	(goldman adj sach$.as.) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:11
S6	9	latency same distributed same API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:23
S7	3	(("6041352") or ("6144961") or ("6108700")).PN.	USPAT; USOCR	OR	OFF	2004/01/29 15:49
S8	5	"6144961".URPN.	USPAT	OR	OFF	2004/01/29 15:44
S9	0	709/328.ccls. and (distributed same measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:52
S10	0	709/328.ccls. and (distributed and measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:52
S11	0	709/328.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:50
S12	0	709/328$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:51
S13	616	719/328$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:51
S14	616	719/328.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:51
S15	3	719/328.ccls. and (distributed same measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:54
S16	50	719/328.ccls. and (distributed and measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:56
S17	23	719/328.ccls. and (distributed and latency)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 15:57
S18	2	719/328.ccls. and (distributed and (latency same measur$5))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:09

S19	0	719/328.ccls. and ((without near3 condition) same measur$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:09
S20	14	API and ((without near3 condition) same measur$5)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:12
S21	96	measur$3 same latency same distributed	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:19
S22	39	(measur$3 same latency same distributed) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:12
S23	39	(US-6381735-$ or US-6381628-$ or US-6347337-$ or US-6266406-$ or US-6263491-$ or US-6230312-$ or US-6421733-$ or US-6499137-$ or US-6629123-$ or US-6460080-$). did. or (US-20030191812-$ or US-20030191800-$ or US-20030188021-$ or US-20030188016-$ or US-20030188009-$ or US-20020198734-$ or US-20020161907-$ or US-20030187935-$ or US-20030101307-$ or US-20030179775-$ or US-20030056199-$ or US-20030056200-$ or US-20030014464-$ or US-20030018494-$ or US-20030004744-$ or US-20030004774-$ or US-20020188538-$ or US-20020178026-$ or US-20020177448-$ or US-20020174191-$ or US-20020173984-$ or US-20020169644-$ or US-20020165745-$ or US-20020072830-$ or US-20020165727-$ or US-20020023143-$).did. or (US-20020147611-$ or US-20020055993-$ or US-20020032804-$).did.	US-PGPUB; USPAT	OR	OFF	2004/01/29 16:18

S24	39	((US-6381735-$ or US-6381628-$ or US-6347337-$ or US-6266406-$ or US-6263491-$ or US-6230312-$ or US-6421733-$ or US-6499137-$ or US-6629123-$ or US-6460080-$). did. or (US-20030191812-$ or US-20030191800-$ or US-20030188021-$ or US-20030188016-$ or US-20030188009-$ or US-20020198734-$ or US-20020161907-$ or US-20030187935-$ or US-20030101307-$ or US-20030179775-$ or US-20030056199-$ or US-20030056200-$ or US-20030014464-$ or US-20030018494-$ or US-20030004744-$ or US-20030004774-$ or US-20020188538-$ or US-20020178026-$ or US-20020177448-$ or US-20020174191-$ or US-20020173984-$ or US-20020169644-$ or US-20020165745-$ or US-20020072830-$ or US-20020165727-$ or US-20020023143-$).did. or (US-20020147611-$ or US-20020055993-$ or US-20020032804-$).did.) and (measur$3 same latency same distributed)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:30
S25	1	709/224.ccls. and (without near2 condition) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:32
S26	454	709/224.ccls. and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:32
S27	306	(709/224.ccls. and API) and distributed	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:32
S28	163	((709/224.ccls. and API) and distributed) and (latency or measure or measurement or measuring)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:33
S29	131	(((709/224.ccls. and API) and distributed) and (latency or measure or measurement or measuring)) and individual	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:33

S30	11	((709/224.ccls. and API) and distributed) and ((latency or measure or measurement or measuring) near5 individual)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:47
S31	761	(calculat$3 or formula) near5 latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:53
S32	33	((calculat$3 or formula) near5 latency) same (transaction)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:49
S33	114	formula near10 latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:56
S34	111	(formula near10 latency) not (((calculat$3 or formula) near5 latency) same (transaction))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:54
S35	3	(formula near10 latency) same transaction$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 16:58
S36	560	measur$3 same transaction$3 same network	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:04
S37	96	(measur$3 same transaction$3 same network) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:03
S38	32	709/224.ccls. and API and (transaction same measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:21
S39	0	"6633908".URPN.	USPAT	OR	OFF	2004/01/29 17:28
S40	19	"6108700".URPN.	USPAT	OR	OFF	2004/01/29 17:43
S41	2	719/328.ccls. and (transaction same measur$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:49
S42	14	719/328.ccls. and (transaction and measurement)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:55
S43	551	measur$3 same transaction same network	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 17:56
S44	92	(measur$3 same transaction same network) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 18:18
S45	3929	international adj business adj machines.as. and ARM	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 18:18
S46	13	(international adj business adj machines.as. and ARM) and transaction and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/29 18:19

S47	0	709/324.ccls. and database and (aggrega$5 or calculat$3) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:09
S48	0	709/324.ccls. and database and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:09
S49	0	709/3224.ccls. and database and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:09
S50	341	709/224.ccls. and database and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:09
S51	196	709/224.ccls. and database and (aggrega$5 or calculat$3) and API	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:09
S52	19	709/224.ccls. and database and ((aggrega$5 or calculat$3) same API)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:12
S53	19	(US-6633908-$ or US-6549943-$ or US-6473794-$ or US-6415322-$ or US-6381640-$ or US-6370508-$ or US-6170011-$ or US-6108700-$ or US-6061724-$ or US-6055493-$ or US-5960439-$).did. or (US-20030171977-$ or US-20030055951-$ or US-20020194305-$ or US-20020147784-$ or US-20020143930-$ or US-20020087682-$ or US-20020049759-$ or US-20020026505-$).did.	US-PGPUB; USPAT	OR	OFF	2004/01/31 14:12
S54	19	((US-6633908-$ or US-6549943-$ or US-6473794-$ or US-6415322-$ or US-6381640-$ or US-6370508-$ or US-6170011-$ or US-6108700-$ or US-6061724-$ or US-6055493-$ or US-5960439-$).did. or (US-20030171977-$ or US-20030055951-$ or US-20020194305-$ or US-20020147784-$ or US-20020143930-$ or US-20020087682-$ or US-20020049759-$ or US-20020026505-$).did.) and ((aggrega$5 or calculat$3) same API)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:25
S55	161	transaction same API same monitor$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:27

S56	61	transaction same API same monitor$3 and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2004/01/31 14:30
S57	31	transaction same API same monitor$3 and latency	USPAT	OR	ON	2004/01/31 14:30
S58	1	"6108700".pn.	USPAT	OR	OFF	2004/01/31 15:24
S64	2	instrument near5 application same ARM and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 17:56
S65	1	add near5 code near5 application same ARM and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:24
S66	3	add near5 code near5 application and ARM and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 17:54
S67	2	S66 not S65 not S64	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 17:54
S68	199	instrument near5 application and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 17:56
S69	0	instrument near5 application near5 add near5 code and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 17:56
S70	5	instrument near5 application near5 code and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:23
S71	0	without near5 predefine$5 near5 event and latency	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:23
S72	435	ARM and latency and add$5 near5 code	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:25
S73	372	ARM and latency and add$5 near5 code and monitor$5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:25
S74	12	ARM and latency and add$5 near5 code and monitor$5 and (transaction near5 event)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:32
S75	3	(("6,041,352") or ("6,144,961") or ("6,108,700")).PN.	USPAT	OR	OFF	2005/05/24 18:48
S76	389	transaction same start near2 time and end near2 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:49
S77	66	transaction same latency and start near2 time and end near2 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:50

S78	4	transaction same latency same start near2 time and end near2 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S79	1	("6415133").PN.	USPAT	OR	OFF	2005/05/24 18:55
S80	4	S78 and transaction same latency same start near2 time and end near2 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S81	0	S79 and transaction same latency same start near2 time and end near2 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S82	0	S79 and transaction same latency same start near5 time and end near5 time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S83	0	S79 and transaction same latency same start same time and end same time	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S84	0	S79 and transaction same latency same start and end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:58
S85	0	S79 and transaction same latency and start and end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:57
S86	1	S79 and transaction and latency and start and end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:58
S87	1	S79 and transaction and latency same start and end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:58
S88	633	S79 latency same start same end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 18:58
S89	1	S79 and latency same start same end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:00
S90	11	latency near formula	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:01
S91	0	latency near formula same transaction	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:01
S92	6	latency near formula and transaction	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:02
S93	3	latency near formula and between near5 component	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:30

S94	59	((transaction$1 event$1) with (time near3 start) with (time near3 end) with (formula calculat$5)) and (add$3 sum$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:30
S95	53	((transaction$1 event$1) with (time near3 start) with (time near3 end) with (formula calculat$5)) and (add$3 sum$4)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:41
S96	45	((transaction$1 event$1) with (time near3 start) with (time near3 end) with (formula calculat$5)) and (add or addition or sum or summation)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:33
S97	3	((transaction$1 event$1) with (time near3 start) with (time near3 end) with (formula calculat$5)) same (add or addition or sum or summation)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:33
S98	0	end adj to adj end same latency	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:41
S99	727	end adj2 end same latency	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:41
S100	3	(end adj2 end) same latency same formula	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:42
S101	1	(end adj2 end) same latency near5 formula	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2005/05/24 19:43
S102	1	(end adj2 end) same latency near5 formula	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:43
S103	138	latency near5 formula	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:43
S104	5	latency near5 formula same add	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:46
S105	21	latency near5 formula same average	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:50
S106	0	latency near5 formula same average same start	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:50
S107	155	latency same average same start	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:50
S108	23	latency same average same start same end	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/05/24 19:50

Terms used **performance monitoring transaction API** Found **910** of **169,866**

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1 Predicting the performance of middleware-based applications at the design level
Yan Liu, Alan Fekete, Ian Gorton
January 2004 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 4th international workshop on Software and performance WOSP '04**, Volume 29 Issue 1
Publisher: ACM Press
Full text available: pdf(713.57 KB) Additional Information: full citation, abstract, references

In this paper, we present an approach to predict the performance of middleware-based applications at the design level. We develop a quantitative performance model for a proposed system design. The inputs needed to produce this performance prediction are a state diagram showing the main waiting and resource usage aspects of the proposed system architecture, and measurements taken on the middleware infrastructure using a simple benchmark application which is much cheaper to implement than the full ...

2 Correlating resource demand information with ARM data for application services
Jerome Rolia, Vidar Vetland
October 1998 **Proceedings of the 1st international workshop on Software and performance WOSP '98**
Publisher: ACM Press
Full text available: pdf(3.90 MB) Additional Information: full citation, references, index terms

3 A J2EE application for process accounting, LPAR accounting, and transaction accounting
C. Eric Wu, William P. Horn
July 2005 **Proceedings of the 5th international workshop on Software and performance WOSP '05**
Publisher: ACM Press
Full text available: pdf(751.27 KB) Additional Information: full citation, abstract, references, index terms

Accounting is critical for information technology budgeting and chargeback. Traditional accounting in UNIX/Linux systems is known as process accounting, in which an accounting record is created when a process ends. System administrators typically aggregate accounting records based on individual users or groups. As Web and application servers along with databases handle requests and transactions for multiple entities in various Web applications and services, LPAR accounting and transaction accoun ...

Keywords: ARM transactions, process accounting, project accounting, resource usage, transaction accounting

4 Flexible control of downloaded executable content

Trent Jaeger, Atul Prakash, Jochen Liedtke, Nayeem Islam
May 1999 **ACM Transactions on Information and System Security (TISSEC)**, Volume 2 Issue 2
Publisher: ACM Press

Full text available: pdf(297.79 KB) Additional Information: full citation, abstract, references, citings, index terms, review

We present a security architecture that enables system and application a ccess control requirements to be enforced on applications composed from downloaded executable content. Downloaded executable content consists of messages downloaded from remote hosts that contain executables that run, upon receipt, on the downloading principal's machine. Unless restricted, this content can perform malicious actions, including accessing its downloading principal's private data and sending messages on th ...

Keywords: access control models, authentication, autorization machanisms, collaborative systems, role-based access control

5 Improving interactive performance using TIPME

Yasuhiro Endo, Margo Seltzer
June 2000 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2000 ACM SIGMETRICS international conference on Measurement and modeling of compute r systems SIGMETRICS '00**, Volume 28 Issue 1
Publisher: ACM Press

Full text available: pdf(1.05 MB) Additional Information: full citation, abstract, references, citings, index terms

On the vast majority of today's computers, the dominant form of computation is GUI-based user interaction. In such an environment, the user's perception is the final arbiter of performance. Human-factors research shows that a user's perception of performance is affected by unexpectedly long delays. However, most performance-tuning techniques currently rely on throughput-sensitive benchmarks. While these techniques improve the average performance of the system, they do littl ...

Keywords: interactive performance, monitoring

6 Systems and prototypes: Java support for data-intensive systems: experiences building the telegraph dataflow system

Mehul A. Shah, Michael J. Franklin, Samuel Madden, Joseph M. Hellerstein
December 2001 **ACM SIGMOD Record**, Volume 30 Issue 4
Publisher: ACM Press

Full text available: pdf(1.38 MB) Additional Information: full citation, abstract, references, citings

Database system designers have traditionally had trouble with the default services and interfaces provided by operating systems. In recent years, developers and enthusiasts have increasingly promoted Java as a serious platform for building data-intensive servers. Java provides a number of very helpful language features, as well as a full run-time environment reminiscent of a traditional operating system. This combination of features and community support raises the question of whether Java is be ...

7 Server performance and scalability: A method for transparent admission control and request scheduling in e-commerce web sites

Sameh Elnikety, Erich Nahum, John Tracey, Willy Zwaenepoel
May 2004 **Proceedings of the 13th international conference on World Wide Web**
Publisher: ACM Press

Full text available: pdf(179.28 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a method for admission control and request scheduling for multiply-tiered e-commerce Web sites, achieving both stable behavior during overload and

improved response times. Our method externally observes execution costs of requests online, distinguishing different request types, and performs overload protection and preferential scheduling using relatively simple measurements and a straight forward control mechanism. Unlike previous proposals, which require extensive changes to ...

Keywords: admission control, dynamic web content, load control, request scheduling, web servers

8 Middleware: a model for distributed system services

Philip A. Bernstein
February 1996 **Communications of the ACM**, Volume 39 Issue 2
Publisher: ACM Press
Full text available: pdf(238.25 KB) Additional Information: full citation, references, citings, index terms

9 Integrating Database Technology with Comparison-based Parallel Performance Diagnosis: The PerfTrack Performance Experiment Management Tool

Karen L. Karavanic, John May, Kathryn Mohror, Brian Miller, Kevin Huck, Rashawn Knapp, Brian Pugh
November 2005 **Proceedings of the 2005 ACM/IEEE conference on Supercomputing SC '05**
Publisher: IEEE Computer Society
Full text available: pdf(746.95 KB) Publisher Site Additional Information: full citation, abstract

PerfTrack is a data store and interface for managing performance data from large-scale parallel applications. Data collected in different locations and formats can be compared and viewed in a single performance analysis session. The underlying data store used in PerfTrack is implemented with a database management system (DBMS). PerfTrack includes interfaces to the data store and scripts for automatically collecting data describing each experiment, such as build and platform details. We have impl ...

10 Using generative design patterns to generate parallel code for a distributed memory environment

Kai Tan, Duane Szafron, Jonathan Schaeffer, John Anvik, Steve MacDonald
June 2003 **ACM SIGPLAN Notices , Proceedings of the ninth ACM SIGPLAN symposium on Principles and practice of parallel programming PPoPP '03**, Volume 38 Issue 10
Publisher: ACM Press
Full text available: pdf(385.41 KB) Additional Information: full citation, abstract, references, index terms

A design pattern is a mechanism for encapsulating the knowledge of experienced designers into a re-usable artifact. Parallel design patterns reflect commonly occurring parallel communication and synchronization structures. Our tools, CO2P3S (Correct Object-Oriented Pattern-based Parallel Programming System) and MetaCO2P3S, use *generative design patterns*. A programmer selects the parallel design patterns that are appropriate for an application, and then adapts the patterns for that specifi ...

Keywords: design patterns, frameworks, parallel programming, programming tools

11 Using Hardware Counters to Automatically Improve Memory Performance

Mustafa M. Tikir, Jeffrey K. Hollingsworth
November 2004 **Proceedings of the 2004 ACM/IEEE conference on Supercomputing**
Publisher: IEEE Computer Society
Full text available: pdf(152.84 KB) Additional Information: full citation, abstract

In this paper, we introduce a profile-driven online page migration scheme and investigate

its impact on the performance of multithreaded applications. We use lightweight, inexpensive plug-in hardware counters to profile the memory access behavior of an application, and then migrate pages to memory local to the most frequently accessing processor. Using the Dyninst runtime instrumentation combined with hardware counters, we were able to add page migration capabilities to the system without having ...

12 Efficient and flexible architectural support for dynamic monitoring

Yuanyuan Zhou, Pin Zhou, Feng Qin, Wei Liu, Josep Torrellas
March 2005 **ACM Transactions on Architecture and Code Optimization (TACO)**, Volume 2 Issue 1
Publisher: ACM Press
Full text available: pdf(524.21 KB) Additional Information: full citation, abstract, references, index terms

Recent impressive performance improvements in computer architecture have not led to significant gains in the case of debugging. Software debugging often relies on inserting run-time software checks. In many cases, however, it is hard to find the root cause of a bug. Moreover, program execution typically slows down significantly, often by 10--100 times.To address this problem, this paper introduces the *intelligent watcher (iWatcher)*, a novel architectural scheme to monitor dynamic executio ...

Keywords: Architectural support, dynamic monitoring, software debugging, thread-level speculation (TLS)

13 Automatic generation of layered queuing software performance models from commonly available traces

Tauseef A. Israr, Danny H. Lau, Greg Franks, Murray Woodside
July 2005 **Proceedings of the 5th international workshop on Software and performance WOSP '05**
Publisher: ACM Press
Full text available: pdf(384.50 KB) Additional Information: full citation, abstract, references

Performance models of software designs can give early warnings of problems such as resource saturation or excessive delays. However models are seldom used because of the considerable effort needed to construct them. Software Architecture and Model Extraction (SAME) is a lightweight model building technique that extracts communication patterns from executable designs or prototypes that use message passing, to develop a Layered Queuing Network model in an automated fashion. It is a formal, traceab ...

Keywords: layered queuing, model building, performance engineering, software performance, tracing performance modeling

14 The new middleware

Rich Finkelstein
December 1995 **ACM SIGMOD Record**, Volume 24 Issue 4
Publisher: ACM Press
Full text available: pdf(591.71 KB) Additional Information: full citation, abstract, index terms

USING MIDDLEWARE, CUSTOMERS CAN DEPLOY COST-EFFECTIVE AND HIGHLY FUNCTIONAL CLIENT/SERVER APPLICATIONS — ONCE THEY WORK OUT THE KINKS.

15 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren
November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**
Publisher: IBM Press
Full text available: pdf(4.21 MB) Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the

execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

16 OceanStore: an architecture for global-scale persistent storage

John Kubiatowicz, David Bindel, Yan Chen, Steven Czerwinski, Patrick Eaton, Dennis Geels, Ramakrishna Gummadi, Sean Rhea, Hakim Weatherspoon, Chris Wells, Ben Zhao

November 2000 **ACM SIGARCH Computer Architecture News , ACM SIGOPS Operating Systems Review , Proceedings of the ninth international conference on Architectural support for programming languages and operating systems ASPLOS-IX**, Volume 28 , 34 Issue 5 , 5

Publisher: ACM Press

Full text available: pdf(166.53 KB) Additional Information: full citation, abstract, references, citings, index terms

OceanStore is a utility infrastructure designed to span the globe and provide continuous access to persistent information. Since this infrastructure is comprised of untrusted servers, data is protected through redundancy and cryptographic techniques. To improve performance, data is allowed to be cached anywhere, anytime. Additionally, monitoring of usage patterns allows adaptation to regional outages and denial of service attacks; monitoring also enhances performance through pro-active movement ...

17 The measured performance of personal computer operating systems

J. Bradley Chen, Yasuhiro Endo, Kee Chan, David Mazières, Antonio Dias, Margo Seltzer, Michael D. Smith

February 1996 **ACM Transactions on Computer Systems (TOCS)**, Volume 14 Issue 1

Publisher: ACM Press

Full text available: pdf(2.38 MB) Additional Information: full citation, abstract, references, citings, index terms

This article presents a comparative study of the performance of three operating systems that run on the personal computer architecture derived form the IBM-PC. The operating systems, Windows for Workgroups, Windows NT, and NetBSD (a freely available variant of the UNIX operating system), cover a broad range of system functionality and user requirements, from a single-address-space model to full protection with preemptive multitasking. Our measurements are enable by hardware counters in Inte ...

Keywords: Microsoft Windows, operating systems performance measurement, operating systems structure, personal computers

18 Optimal allocation of on-chip memory for multiple-API operating systems

D. Nagle, R. Uhlig, T. Mudge, S. Sechrest

April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture ISCA '94**, Volume 22 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(1.27 MB) Additional Information: full citation, abstract, references, citings, index terms

The allocation of die area to different processor components is a central issue in the design of single-chip microprocessors. Chip area is occupied by both core execution logic, such as ALU and FPU datapaths, and memory structures, such as caches, TLBs, and write buffers. This work focuses on the allocation of die area to memory structures through a cost/benefit analysis. The cost of memory structures with different sizes and associativities is estimated by using an established area model for on ...

19 OceanStore: an architecture for global-scale persistent storage

John Kubiatowicz, David Bindel, Yan Chen, Steven Czerwinski, Patrick Eaton, Dennis Geels, Ramakrishan Gummadi, Sean Rhea, Hakim Weatherspoon, Westley Weimer, Chris Wells, Ben

Zhao
November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11
Publisher: ACM Press
Full text available: pdf(1.47 MB) Additional Information: full citation, abstract, references, index terms

OceanStore is a utility infrastructure designed to span the globe and provide continuous access to persistent information. Since this infrastructure is comprised of untrusted servers, data is protected through redundancy and cryptographic techniques. To improve performance, data is allowed to be cached anywhere, anytime. Additionally, monitoring of usage patterns allows adaptation to regional outages and denial of service attacks; monitoring also enhances performance through pro-active movement ...

20 Cloning parallel simulations

Maria Hybinette, Richard M. Fujimoto
October 2001 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 11 Issue 4
Publisher: ACM Press
Full text available: pdf(1.88 MB) Additional Information: full citation, abstract, references, citings, index terms

We present a cloning mechanism that enables the evaluation of multiple simulated futures. Performance of the mechanism is analyzed and evaluated experimentally on a shared memory multiprocessor. A running parallel discrete event simulation is dynamically cloned at *decision points* to explore different execution paths concurrently. In this way, what-if and alternative scenario analysis can be performed in applications such as gaming or tactical and strategic battle management. A construct c ...

Keywords: Cloning, multiprocessors, parallel algorithms, parallel simulation, pruning

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